Day : Monday Date: 4/28/2008 Time: 11:14:27

Inventor Name Search Result

Your Search was:

Last Name = PORET First Name = MICHEL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
07171067	Not Issued	166	03/21/1988	PROTOCOL AND APPARATUS FOR A CONTROL LINK BETWEEN A CONTROL UNIT AND SEVERAL DEVICES	PORET, MICHEL
07176134	Not Issued	166	03/31/1988	ADAPTER-BUS SWITCH FOR IMPROVING THE AVAILABILITY OF A CONTROL UNIT	PORET, MICHEL
07294456	4972345	150	01/09/1989	APPARATUS FOR ERROR DETECTION AND REPORTING ON A SYNCHRONOUS BUS	PORET, MICHEL
07573108	5128666	150	02/20/1990	PROTOCOL AND APPARATUS FOR A CONTROL LINK BETWEEN A CONTROL UNIT AND SEVERAL DEVICES	PORET, MICHEL
07577837	5140691	150	08/30/1990	ADAPTER-BUS SWITCH FOR IMPROVING THE AVAILABILITY OF A CONTROL UNIT	PORET, MICHEL
08992232	6055235	150	12/17/1997	SWITCHING SYSTEM	PORET, MICHEL
08992871	6108334	150	12/17/1997	SWITCHING SYSTEM COMPRISING DISTRIBUTED ELEMENTS ALLOWING ATTACHMENT TO LINE ADAPTERS	PORET, MICHEL
09018591	6324164	250	02/04/1998	ASYNCHRONOUS TRANSFER MODE (A.T.M.) PROTOCOL ADPTER FOR A HIGH SPEED CELL SWITCHING SYSTEM	PORET, MICHEL
09204394	6411599	150	12/02/1998	FAULT TOLERANT SWITCHING ARCHITECTURE	PORET, MICHEL
09238879	6424632	150	01/27/1999	METHOD AND APPARATUS FOR TESTING PACKET DATA INTEGRITY USING DATA CHECK FIELD	PORET, MICHEL
09315446	6661786	150	05/20/1999	SERVICE MESSAGE SYSTEM FOR A SWITCHING	PORET, MICHEL

				ARCHITECTURE	
09315906	6728251	150	05/20/1999	SWITCHING APPARATUS COMPRISING CENTRALIZED SWITCH CORE AND AT LEAST ONE SCAL ELEMENT(S) FOR THE ATTACHMENT OF VARIOUS PROTOCOL ADAPTERS	PORET, MICHEL
10243294	7289523	150	09/12/2002	DATA PACKET SWITCH AND METHOD OF OPERATING SAME	PORET, MICHEL
10249182	7134067	150	03/20/2003	APPARATUS AND METHOD FOR ALLOWING A DIRECT DECODE OF FIRE AND SIMILAR CODES	PORET, MICHEL
10512671	Not Issued	41	08/04/2005	Method and arrangement for local sychronization in master-slave distributed communication systems	PORET, MICHEL
10711329	Not Issued	71	09/10/2004	METHOD AND SYSTEM TO ENABLE AN ADAPTIVE LOAD BALANCING IN A PARALLEL PACKET SWITCH	PORET, MICHEL
10722900	Not Issued	30	11/26/2003	System and method for re- sequencing data packets on a per- flow basis	PORET, MICHEL
10722901	Not Issued	71	11/26/2003	Method and system for resequencing data packets switched through a parallel packet switch	PORET, MICHEL
10723503	Not Issued	93	11/26/2003	PACKET UNSTOPPER SYSTEM FOR A PARALLEL PACKET SWITCH	PORET, MICHEL
10723834	Not Issued	93	11/26/2003	CAM BASED SYSTEM AND METHOD FOR RE- SEQUENCING DATA PACKETS	PORET, MICHEL
10764247	7284184	150	01/23/2004	FORWARD ERROR CORRECTION SCHEME COMPATIBLE WITH THE BIT ERROR SPREADING OF A SCRAMBLER	PORET, MICHEL
10894582	Not Issued	71	07/20/2004	System and method for collapsing VOQ'S of a packet switch fabric	PORET, MICHEL
10894681	Not Issued	30	07/20/2004	Algorithm and system for selecting acknowledgments from an array of collapsed VOQ's	PORET, MICHEL
10895159	Not Issued	30	07/20/2004	System and method for handling multicast traffic in a shared buffer switch core collapsing ingress VOQ's	PORET, MICHEL

11776474	Not Issued	17	FORWARD ERROR CORRECTION SCHEME COMPATIBLE WITH THE BIT ERROR SPREADING OF A SCRAMBLER	PORET, MICHEL
11852661	Not Issued	30	 Data Packet Switch and Method of Operating Same	PORET, MICHEL

Inventor Search Completed: No Records to Display.

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